

10/531340 #2

JC20 Rec'd PCT/PTO 14 APR 2005

Express Mail Label No. EV 492322708 US
Attorney Docket No. S1022.81221US00
Mailed : April 14, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Laurent REGNIER
Serial No: Unassigned
Filed: April 14, 2005
For: REPEAT DIGITAL MESSAGE TRANSMISSION BETWEEN A
MICROPROCESSOR MONITORING CIRCUIT AND AN ANALYZING TOOL

Examiner: Unassigned
Art Unit: Unassigned
Confirmation No: Unassigned

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement is being concurrently with the entry of the National Stage, as set forth in 37 C.F.R. §1.491, in an International application.

No fee or certification is required.

Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant would like to bring to the Examiner's attention the following co-pending applications that may contain subject matter related to this application:

<u>Express Mail Label No.</u>	<u>Filing Date</u>	<u>Inventor(s)</u>
EL 565140513 US	April 14, 2005	Laurent Regnier
EV 292457568 US	April 14, 2005	Laurent Regnier
EL 292457554 US	April 14, 2005	Laurent Regnier
EV 492322699 US	April 14, 2005	Laurent Regnier

[NOTE TO PRACTITIONERS - Must provide a copy of any pending U.S. application, even if it was previously submitted to or cited by the U.S. Patent Office in an earlier application, unless the earlier application is identified herein and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]

The Applicant would like to bring to the Examiner's attention the enclosed International Search report from corresponding International Application No. PCT/FR02/03526.

The above-identified application(s)/patent(s) may include technically-related subject matter and/or claims that are similar to this application. The examiner is requested to review the entire file history of the above identified application(s)/patent(s), including cited references, Office Actions, Responses, etc. The examiner is asked to contact the undersigned if the examiner would like the applicant to supply copies of any or all of the information included in any of the above-listed application(s)/patent(s). If the undersigned is not contacted by the examiner with such a request, then the undersigned will assume that the examiner has reviewed or will review the file content of the above-listed application(s)/patent(s).

As shown in the patent family search attached to the search report, U.S. Patent No. 6,519,766 corresponds to document WO 0077643 and U.S. Patent No. 5,621,663 corresponds to document WO 9300632, cited in the International Search Report.

Explanation of Non-English Language References and Remarks Concerning Other Information Cited

The following is a concise explanation of the relevance of each non-English language reference listed on the attached form PTO-1449 (modified):

For the relevance of the listed non-English language reference, the examiner's attention is drawn to the enclosed International Search Report, wherein all of the cited documents have been placed in category "A", meaning that, in the opinion of the International Searching Authority, they relate to the general technological field of the invention.

REMARKS

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

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An early and favorable action is hereby requested.

Respectfully submitted,

Laurent REGNIER, Applicant

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Docket No.: S1022.81221US00

Date: April 14, 2005

FORM PTO-1449/A and B (Modified)

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

APPLICATION NO.: Unassigned	ATTY. DOCKET NO.: S1022.81221US00
FILING DATE: April 14, 2005	CONFIRMATION NO.: Unassigned
APPLICANT: Laurent REGNIER	
GROUP ART UNIT: Unassigned	EXAMINER: Unassigned

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
		5,237,684		Record et al.	08/17/1993
		5,621,663		Skagerling	04/15/1997
		5,996,092		Augsburg et al.	11/30/1999
		6,167,536		Mann	12/26/2000
		6,332,117	B1	Berry et al.	12/18/2001
		6,467,083	B1	Yamashita	10/15/2002
		6,519,766	B1	Barritz et al.	02/11/2003

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			
		EP	1 184 790	A2	Mann	03/06/2002	

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		International Search report from corresponding Intn'l Application No. PCT/FR02/03526, filed October 15, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03723, filed October 29, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03725, filed October 29, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03521, filed October 15, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03724, filed October 29, 2002	
		Plauser, D., <u>Real-Time Unix: Timing is Everything</u> , Mini Micro Systems, Cahners Publishing CY, Boston, vol. 22, no. 2, February 1, 1989, pages 72-76, XP000039292	
		Okeeffe, H. et al., <u>Einheitliche Debug-Schnittstelle Der Zukunft Der Nexus-Standard Soll Die Kommunikation Zwischen Emulator Und Prozessor Vereinheitlichen</u> , Elektronik, Franzis Verlag GMBH, Munchen, vol. 49, no. 18, Sept. 5, 2000, pages 124-128, XP001107530	
		Macnamee, C., <u>Ein Interface Fuer Alle Nexus Als Globaler Debug-Standard</u> , Elektronikpraxis, Vogel, Wuerzburg, no. 3, Feb. 8, 2000, pages 104-108, XP008001346	
		Schmitt, W., <u>Nexus - Debug Konzept Der Zukunft? Universelle Emulations-Und Kalibrierschnittstelle Fuer Mikrocontroller</u> , Elektronik, Franzis Verlag GMBH, Munchen, vol. 48, no. 17, Aug. 24, 1999, pages 52-59, XP000931028	
		Nexis 5001 Forum: <u>Standard for a Global Embedded Processor Debug Interface</u> , Dec, 15, 1999, IEEE-ISTO XP002247195	

EXAMINER:

DATE CONSIDERED:

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC 371 after June 30, 2003. See 37 CFR 1.491(b). For all patent applications filed on or before June 30, 2003, copies of cited U.S. patents and patent application publications are still required unless an IDS is filed. Copies of all other patent(s), publication(s), or other information listed must still be provided, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.